**PATENT** 



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Harsh D. Sharma and Nayon Tomsio

Assignee:

Sun Microsystems, Inc.

Title:

PRIORITY DELAY INSERTION CIRCUIT

Patent No.:

7,016,422

Issued:

March 21, 2006

Atty. Docket No.: 004-6183

May 5, 2006

ATTN: Certificate of Correction Branch COMMISSIONER FOR PATENTS P.O. Box 1450

Alexandria, VA 22313-1450

## REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT – PTO MISTAKE (37 C.F.R. § 1.322(a))

Dear Sir:

Pursuant to 35 U.S.C. § 254 and 37 C.F.R. § 1.322(a), please issue a Certificate of Correction in the above-identified matter. The mistake(s) to be corrected was made by the Office.

- 1. Attached hereto is Form PTO/SB/44 (PTO-1050) suitable for printing.
- 2. The exact page(s) and line number(s) where the error(s) is shown correctly in the application file:

Response to Non-Final Office action dated 5/31/05, page 2;

Application as filed, page 5, line 31;

Response After Final Office action dated 9/27/05, page 6;

Examiner Amendment included with Notice of Allowance dated 10/20/05, page 3;

3. Please send the Certificate to:

Nicole Teitler Cave ZAGORIN O'BRIEN GRAHAM LLP 7600B N. CAPITAL OF TEXAS HWY, SUITE 350 AUSTIN, TEXAS 78731-1191

Certificate MAY 1 2 2006 of Correction

MAY 12 2006

Respectfully submitted,

/Nicole Teitler Cave, Reg. No. 54,021

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## UNITED STATES PATENT AND TRADEMARK OFFICE

## **CERTIFICATE OF CORRECTION**

PATENT NO : 7,016,422 B2

DATED : March 21, 2006

INVENTOR(S): Harsh D. Sharma and Nayon Tomsio

It is certified that error(s) appear(s) in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 2, line 54, replace "direction" with --directions--;

In column 4, line 8, replace "PDTC" with --PDIC--;

In column 6, line 41, replace "comprising" with --comprising:--;

In column 7, line 13, replace "associating" with --associated--; line 15, replace "disabling signal receive" with --disable signal received--.

MAN 12 AME